Electronic Version
Stylesheet Version v1.1.1

Claims

[c1] What is claimed is:

1.A driving circuit for driving an organic light emitting diode (OLED), the driving circuit comprising:

a first transistor, having a drain and a source connected to a power voltage and the OLED respectively;

an inverter, having an output connected to a gate terminal of the first transistor;

an input circuit for inputting data, having an output connected to an input of the inverter; and

a voltage-reducing circuit, connected to the output of the input circuit for reducing a voltage at the output of the input circuit.

[c2] 2.The driving circuit of claim 1, wherein the first transistor is a thin film transistor (TFT).

[c3] 3.The driving circuit of claim 1, wherein the inverter is a CMOS inverter.

[c4] 4.The driving circuit of claim 1, wherein the input circuit comprises:

a second transistor, having source and drain connected to the input and the output of the input circuit respec-

tively; and

a capacitor, having a first end connected to the output of the input circuit.

[c5] 5.The driving circuit of claim 1, wherein the voltage-reducing circuit comprises a third transistor.

[c6] 6.A driving circuit for driving an OLED, the driving circuit comprising:

a first transistor, having drain and source connected to a power voltage and the OLED respectively;

a comparator, having a first input and an output connected to a reference voltage and to a gate terminal of the first transistor respectively;

an input circuit for inputting data, having an output connected to a second input end of the comparator; and

a voltage-reducing circuit, connected to the output of the input circuit for reducing a voltage at the output of the input circuit.

[c7] 7.The driving circuit of claim 6, wherein the first transistor is a TFT.

[c8] 8.The driving circuit of claim 6, wherein the input circuit comprises:

a second transistor, havingsource and drain connected to an input and the output of the input circuit respec-

tively; and

a capacitor, having a first end connected to the output of the input circuit.

[c9] 9.The driving circuit of claim 6, wherein the voltage-reducing circuit comprises a third transistor.